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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR Danial S. Dean 3318.6US (97-0527.06/US) 10/633,956 08/04/2003

TITLE OF INVENTION: DEVICE FOR MARGIN TESTING A SEMICONDUCTOR MEMORY BY APPLYING A STRESSING VOLTAGE SIMULTANEOUSLY TO COMPLEMENTARY AND TRUE DIGIT LINES

APPLN. TYPE	SMALL ENTITY	ISSUE FE	E	PUBLICATION FEE	TOTAL F	EE(S) DUE	DATE DUE
nonprovisional	NO	· \$1330		\$300	\$1	630	07/30/2004
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MAI, S	SON LUU	2818		365-202000	_		
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